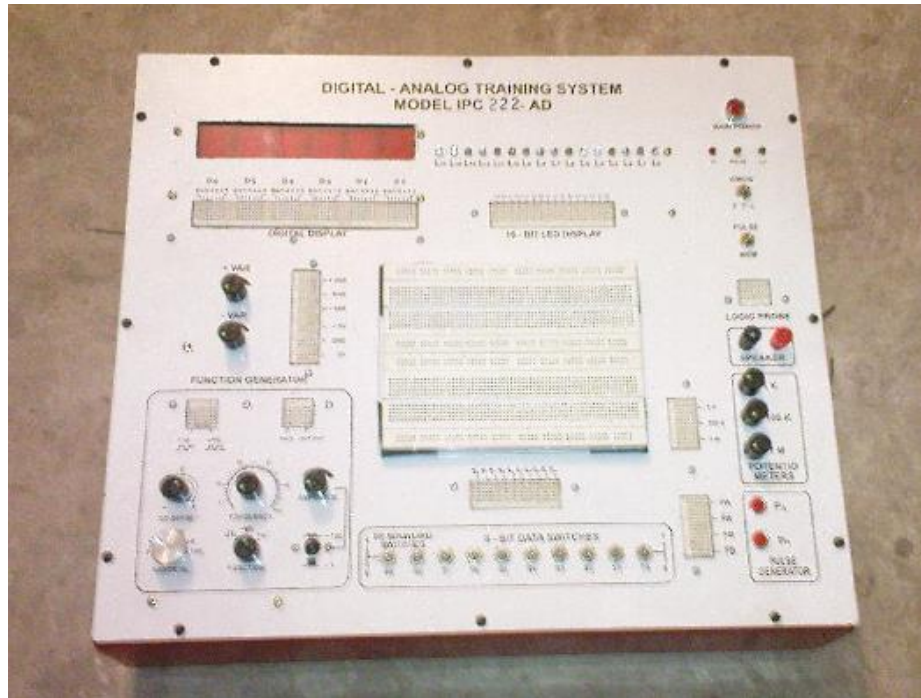


IPC EDUCATIONAL TRAINERS



ANALOG / DIGITAL TRAINER MODEL: - IPC-122-AD

SPECIFICATION:

- **DC FIXED & VARIABLE POWER SUPPLY**
±5 Volts @ 1.0A
Variable 0±20 Volts @ 1.0A
- **Function Generator:**
1Hz to 100 KHz continuously variable over 5 decade ranges.
Sine wave, square wave, triangle wave.
- 3 state logic probe.
- Seven segment display
- Two single shot pulse generator, 80 μs.
- 16 bit LED display with buffer.
- 2 ¼ inch 8Ω, 1 w loud speaker.
- Two logic switches ±5 V/0V with current limit.
- 8 data switch +5V/0V.
- Potentiometer 1K, 100K, 1M
- Double size bread board.

Note: - we reserve the right to change the shape & design of the trainer without prior notice.
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The Experiments of Analog Circuits:

- The Superposition Theorem
- Capacitor in Voltage – divider networks
- Operational Amplifier- The Inverting Amplifier
- Operational Amplifier- The Non-inverting Amplifier
- Operational Amplifier- The Comparator
- Operational Amplifier- The Summing Amplifier
- The Common-Base Amplifier Structure
- The Common-Emitter Amplifier Structure
- The Common-Collector Amplifier Structure
- The Op Amp Differentiator
- The Op Amp Integrator
- The RC Phase Shift Oscillator
- The Astable Multivibrator- 555 Timer
- The Schmitt Trigger
- The Astable Multivibrator
- The D/A Converter
- The A/D Converter

The Experiments of Digital Circuits:

- Fundamental Logic Gate – AND, OR, NOT.
- Fundamental Logic Gate – NAND, NOR, XOR.
- Applications of Boolean Algebra.
- De Morgan's Law.(1)
- De Morgan's Law.(2)
- Diode Resistor Logic – AND
- Exclusive OR Using Basic Logic Gate.
- Exclusive NOR Using Basic Logic Gate.
- Demultiplexer – Using The 74138 IC.
- Synchronous Up-Counter.
- Synchronous Down-Counter.
- The Schmitt Trigger.
- Oscillator- Using CMOS.